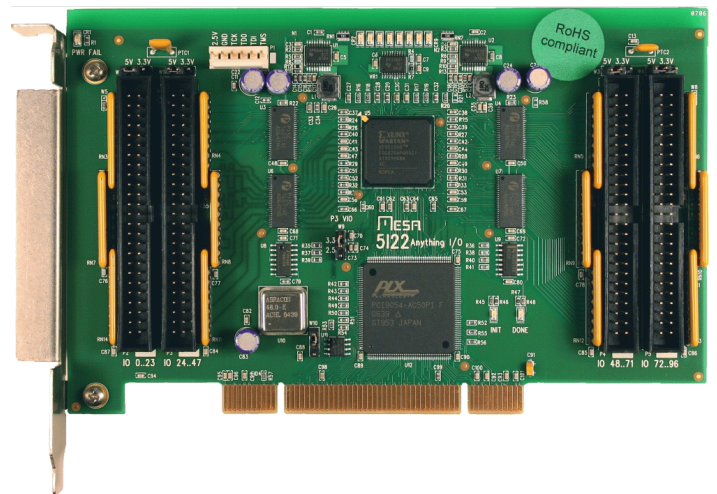


5122 ANYTHING I/O CARD

- Xilinx FPGA based I/O card
- 96 I/O BITS
- 5V tolerant I/O
- 24 mA output drive
- PCI interface
- Bus mastering for high bandwidth
- 11 LED status indicators
- Source supplied for all functions
- FPGA downloaded from host
- Low power consumption
- Made in USA - local support
- 2 year warranty



The MESA 5122 is a general purpose programmable I/O card for the PCI bus. The 5122 uses a 1M or 1.5M gate Xilinx FPGA for all logic, so it is truly an "Anything I/O" card.

The FPGA configuration is downloadable from the PCI bus side, allowing creation of almost any kind of specialized I/O function, even including multiple 32 bit processors in the FPGA.

A bus mastering PCI bridge chip is used for FPGA interfacing, so that high performance FPGA designs need only interface to a simple 32 bit synchronous local bus.

Several pre-made functions are provided, including a 96 bit parallel I/O card with 32 bit host interface, a 16 channel host based servo motor controller, a 4 or 8 channel micro-controller based servo motor controller (micro-controller CPU built into FPGA), and a 16 channel, 32 bit timer counter card capable of running at 100 MHz.

VHDL source is provided for all examples and custom I/O functions can be provided on a contract basis.

All I/O bits are 5V tolerant and can sink 24 mA. Pullup resistors are provided for all pins so that they may be connected directly to optoisolators, contacts etc.

the 5122 uses four 50 pin connectors with I/O module rack compatible pinouts and interleaved grounds. One connector can use 2.5VCCIO and supports 10 LVDS pairs for high speed card-card communication.

2 FPGA configuration status LEDs plus 8 general purpose LEDs are available for circuit debugging or circuit status information.

A 48 MHz crystal oscillator is provided as the FPGA system clock and local bus clock. Higher or lower frequencies can be generated by the DLL built into the FPGA.

5I22 CONNECTOR PINOUT:

PIN#	5I22 SIGNAL (P2,P3,P4,P5)	PIN#	5I22 SIGNAL
1	I/O 0,24,48,72	2	GND
3	I/O 1,25,49,73	4	GND
5	I/O 2,26,50,74	6	GND
7	I/O 3,27,51,75	8	GND
9	I/O 4,28,52,76	10	GND
11	I/O 5,29,53,77	12	GND
13	I/O 5,30,54,78	14	GND
15	I/O 7,31,55,79	16	GND
17	I/O 8,32,56,80	18	GND
19	I/O 9,33,57,81	20	GND
21	I/O 10,34,58,82	22	GND
23	I/O 11,35,59,83	24	GND
25	I/O 12,36,60,84	26	GND
27	I/O 13,37,61,85	28	GND
29	I/O 14,38,62,86	30	GND
31	I/O 15,39,63,87	32	GND
33	I/O 16,40,64,88	34	GND
35	I/O 17,41,65,89	36	GND
37	I/O 18,42,66,90	38	GND
39	I/O 19,43,67,91	40	GND
41	I/O 20,44,68,92	42	GND
43	I/O 21,45,69,93	44	GND
45	I/O 22,46,70,94	46	GND
47	I/O 23,47,71,95	48	GND
49	POWER	50	GND

ORDERING INFORMATION:

5I22-1 Anything I/O PCI 96 bit - 4X 50 pin connectors 1M gate FPGA
 5I22-1.5 Anything I/O PCI 96 bit - 4X 50 pin connectors 1.5M gate FPGA
 Add -I for industrial temperature range.