

# **4I27A MOTOR CONTROLLER - ANALOG**

## **VERSION 1.2**

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## TABLE OF CONTENTS

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### WARNINGS

Servo hazards .....	4
Handling precautions .....	4

### INTRODUCTION

General .....	5
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### HARDWARE CONFIGURATION

General .....	6
Default configuration .....	6
Port address .....	6
LM628 clock .....	8
Default motor-enable polarity .....	8

### INTERFACE CONNECTOR

General .....	9
I/O connector orientation .....	9
RS-422 interface connector pinout .....	9

### INSTALLATION

General .....	10
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### OPERATION

Register map .....	11
Parallel port .....	11
Parallel port bits .....	12
Parallel port operation .....	13
Interrupt select .....	13
Interrupt mode .....	13
Interrupt operation .....	14
Connecting motors .....	14
Servo system tuning .....	15
Demonstration programs .....	16

### REFERENCE INFORMATION

Specifications .....	17
Warranty .....	18
Schematic diagrams .....	19

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## WARNINGS

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### SERVO WARNING

**Large servo motors are capable of inflicting serious injury both to people and to mechanisms associated with the servo system. In addition, some motors use potentially lethal supply voltages.**

**When a servo control system is first configured, unpredictable behavior should be EXPECTED. First time checks of basic servo operation (such as motor position versus drive) should be checked with the motor power leads disconnected.**

**Never depend on software commands to the 4I27A to disable the motor when you or others would be exposed to a hazard should the motor start unexpectedly. Motor power should always be removed when working on mechanical parts of the servo system.**

### STATIC ELECTRICITY

The CMOS integrated circuits on the 4I27A can be damaged by exposure to electrostatic discharges. The following precautions should be taken when handling the 4I27A to prevent possible damage.

- A. Leave the 4I27A in its antistatic bag until needed.
- B. All work should be performed at an antistatic workstation.
- C. Ground equipment into which 4I27A will be installed.
- D. Ground handling personnel with conductive bracelet through 1 megohm resistor to ground.
- E. Avoid wearing synthetic fabrics, particularly Nylon.

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## INTRODUCTION

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### GENERAL

The 4I27A is a low cost, LM628 based 2 axis DC servo motor control system implemented on a stackable PC/104 bus card. The 4I27A is designed for high performance positioning systems using DC servo motors with quadrature shaft encoders.

The per axis output of the 4I27A is a +/- 10 volt signal that can drive analog input type servo amplifiers directly.

The 4I27A quadrature encoder and index inputs are RS-422. This is desirable where long encoder cables are required.

Control signals for each axis include 3 auxiliary I/O bits. These I/O bits may be used for over-temperature shutdown detect and servo amplifier enable. Eight general purpose I/O bits are available for any application use.

The LM628's used on the 4I27A are high performance digital processors specifically designed for motion control. The LM628 can execute a ramp-up, slew, and ramp-down motion sequence without host processor intervention.

Host interrupts can be generated at end of motion, position breakpoints, index pulse, or in response to various error conditions. Interrupts are or'ed on the 4I27A card, so that only one system interrupt is used. The IRQ line used can be software selected from any of the 11 available AT bus interrupts.

A digital PID filter is used to set loop feedback parameters for stability and optimum performance. Velocity, target position and filter parameters may be changed during motion. The clock speed of the LM628's can be lowered to accommodate large motors that require longer time constants.

Demonstration software includes examples of 2 axis position mode operation, velocity mode operation, and a simple filter tuning program that allows dynamic filter coefficient modification while providing a graphic display of the servo system step response. Source code is provided for all demonstration software.

A PWM output version of the 4I27A is available as the 4I27.

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## HARDWARE CONFIGURATION

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### GENERAL

The 4I27A has only three hardware configurable options, the I/O port address, the LM628 clock speed, and the startup motor enable bit states. The options are selected with shorting jumpers placed on three pin headers. In the following discussions, when the words "up", "down", "right", and "left" are used it is assumed that the 4I27A card is oriented with its bus connectors J1 and J2 at the bottom edge of the card (nearest the person doing the configuration).

### DEFAULT CONFIGURATION

The 4I27A card is configured in the following manner when shipped from the factory.

FUNCTION	DEFAULT	JUMPERS	POSITION
PORT ADDRESS	0200H	W1,W2,W3	DOWN,DOWN,DOWN
LM628 CLOCK	8 MHZ	W4	LEFT
MOTORENABLES	PULLED-UP	W5	UP

*Demonstration programs shipped with the 4I27A depend on the port address being left at the default setting*

### PORT ADDRESS

The 4I27A occupies 16 contiguous locations in I/O space. The base address of the 4I27A card can be any of eight different locations. These locations are selected with shorting jumpers placed on jumper blocks W1, W2, and W3.

The following table shows the I/O base addresses selected by these jumpers:

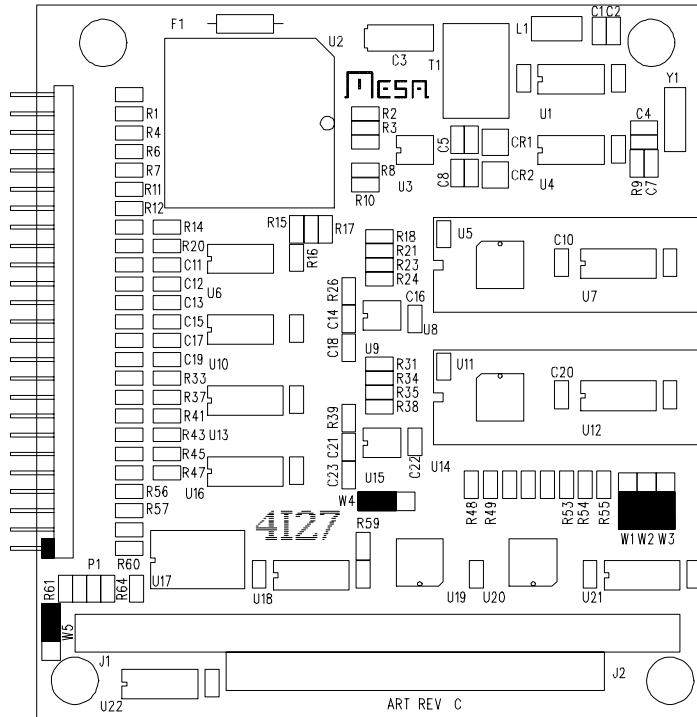
ADDRESS	W1	W2	W3
0200H	DOWN	DOWN	DOWN
0210H	DOWN	DOWN	UP
0220H	DOWN	UP	DOWN
0230H	DOWN	UP	UP
0240H	UP	DOWN	DOWN
0250H	UP	DOWN	UP
0260H	UP	UP	DOWN
0270H	UP	UP	UP

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## HARDWARE CONFIGURATION

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### PORT ADDRESS, CLOCK, AND MOTOR ENABLE JUMPERS



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## HARDWARE CONFIGURATION

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### LM628 CLOCK

The LM628 motor controller chip on the 4I27A normally uses a 8 MHz clock. This clock can be set to 4 MHz if desired. The maximum quadrature input rate will be reduced to 500 KHz when the 4 MHz clock option is selected. Moving W4 to the LEFT position sets the LM628 clock to 8Mhz. Setting W4 to the RIGHT position sets the LM628 clock to 4 MHz.

### DEFAULT MOTOR-ENABLE POLARITY

The motor enable bits on port A of the parallel port can be set to be pulled up or pulled down on power up and system reset. These bits can then be used to disable an external servo amplifier at startup to prevent unwanted motor motion.

The parallel port reverts to an input state when reset, so the status of these pins is determined entirely by the pull-up or pull down resistors.

Setting W5 to the UP position connects the resistors to VCC so that these signals will be high at system reset. This is the default connection. Setting W5 in the DOWN position will pull the motor enable bits down at system reset. Note that these are 3.3K resistors and will only drive a CMOS input until the parallel port is programmed to output mode.

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## INTERFACE CONNECTOR

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### GENERAL

The I/O interface connector on the 4I27A is a 50 pin header. The suggested mating connector is AMP PN 1-746285-0. The 4I27A uses RS-422 encoder inputs.

### I/O CONNECTOR ORIENTATION

The 50 pin connector on the 4I27A has the pin one end marked with a white square on the circuit card. This corresponds with the red stripe on typical flat cable assemblies. If more positive polarization is desired, center polarized IDC header connectors should be used. These connectors will not fully mate with the pins on the 4I27A if installed backwards. A suggested center polarized 50 pin IDC header is AMP PN 1-746285-0.

### INTERFACE CONNECTOR PINOUT

PIN#	SIGNAL	PIN#	SIGNAL
1	MOTOR1QB	2	MOTOR1/QB
3	MOTOR1QA	4	MOTOR1/QA
5	MOTOR0QB	6	MOTOR0/QB
7	MOTOR0QA	8	MOTOR0/QA
9	MOTOR1 IDX	10	MOTOR1/IDX
11	MOTOR0 IDX	12	MOTOR0/IDX
13	MOTOR1AOUT	15	MOTOR0AOUT
17	NC	19	NC
21	/MOTOR1ENA	23	/MOTOR0ENA
25	MOTOR1SENSE1	27	MOTOR0SENSE1
29	MOTOR1SENSE0	31	MOTOR0SENSE0
33	BIT 7	35	BIT 6
37	BIT 5	39	BIT 4
41	BIT 3	43	BIT 2
45	BIT 1	47	BIT 0
49	+5 volt power		

Even numbered pins 14 through 50 are grounded. RS-422 differential pairs are on odd and even successive pins, for example: MOTOR0QB and MOTOR0/QB (pins 5 and 6 respectively) comprise a RS-422 differential pair ( the B quadrature encoder input for motor 0). All RS-422 inputs on the 4I27A are terminated at the 4I27A card with 136 Ohm resistors.

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## INSTALLATION

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### GENERAL

When the 4I27A has been properly configured for its application, it can be inserted into a PC/104 stack. The standoffs should then be tightened to secure the 4I27A in its place. When the 4I27A is secured in the stack the I/O connector can be plugged in from the side.

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## OPERATION

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### REGISTER MAP

The 4I27A occupies 16 contiguous I/O port locations starting at the user selected base address.

BASE +00H	82C55 Port A	(must be output)
BASE +01H	82C55 Port B	(must be input)
BASE +02H	82C55 Port C	(user defined)
BASE +03H	82C55 Control port	
BASE +04H	Motor 0 Command/Status port	
BASE +05H	Motor 0 Data port	
BASE +06H	Motor 0 Command/Status port (duplicate)	
BASE +07H	Motor 0 Data port (duplicate)	
BASE +08H	Motor 1 Command/Status port	
BASE +09H	Motor 1 Data port	
BASE +0AH	Motor 1 Command/Status port (duplicate)	
BASE +0BH	Motor 1 Data port (duplicate)	
BASE +0CH	Motor 0 and Motor 1 Command/Status port	
BASE +0DH	Motor 0 and Motor 1 Data port	
BASE +0EH	Motor 0 and Motor 1 Command/Status port (duplicate)	
BASE +0FH	Motor 0 and Motor 1 Data port (duplicate)	

### PARALLEL PORT

A 82C55 parallel port is supplied to control various bits for motor control and to provide an 8 bit general purpose input or output port. The motor control bits can be used for any purpose. The motor control bits are active low, and have pullup resistors, so that they are inactive on power up. All bits not mentioned are not used and should be set high.

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## OPERATION

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### PARALLEL PORT BITS

82C55 PORT	BIT	PIN	FUNCTION
A	0	23	/Motor 0 enable (general purpose bit)
A	1	21	/Motor 1 enable (general purpose bit)
A	2	N/A	ISEL0
A	3	N/A	ISEL1
A	4	N/A	ISEL2
A	5	N/A	ISEL3
A	6	N/A	IMODE0
A	7	N/A	IMODE1
B	0	31	Motor 0 sense (general purpose bit)
B	1	27	/Motor 0 overtemp (general purpose bit)
B	2	29	Motor 1 sense (general purpose bit)
B	3	25	/Motor 1 overtemp (general purpose bit)
B	4	11	Motor 0 index (also connected to LM628)
B	5	9	Motor 1 index (also connected to LM628)
B	6	N/A	Motor 0 INT status (Rev. C or > cards only)
B	7	N/A	Motor 1 INT status (Rev. C or > cards only)
C	0	47	Bit 0 (general purpose bit)
C	1	45	Bit 1 (general purpose bit)
C	2	43	Bit 2 (general purpose bit)
C	3	41	Bit 3 (general purpose bit)
C	4	39	Bit 4 (general purpose bit)
C	5	37	Bit 5 (general purpose bit)
C	6	35	Bit 6 (general purpose bit)
C	7	33	Bit 7 (general purpose bit)

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## OPERATION

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### PARALLEL PORT OPERATION

The parallel ports on the 4I27A use a standard 82C55 PIA. The user accessible I/O lines have 3.3K pullup resistors to simplify interfacing to contact closure, opto-detector or open collector outputs.

Ports A and B have predefined functions, while port C is available for general purpose use. Port C has the feature of being splittable into two 4 bit ports to allow both input and output bits on the same port.

Port A is used as an output port. It controls the /MOTOR0ENA and /MOTOR1ENA bits, the ISEL bits which determine the IRQ line driven by the 4I27A, and the IMODE bits which control the interrupt driver enable and mode.

Jumper W5 determines whether /MOTOR0ENA and /MOTOR1ENA have pullups or pulldowns. Before setting port A to output mode, Port A can be read to determine the desired 'off' state of these pins. (as determined by W5)

Port B is used as an input port, The MOTOR0SENSE, MOTOR1SENSE, MOTOR0TEMP, MOTOR1TEMP, MOTOR0IDX, MOTOR1IDX, INT0, and INT1 bits are read here. The Pascal source code in the file 4I27LOW.PAS has examples of setting up and using the predefined parallel I/O on the 4I27A.

### INTERRUPT SELECT

When the 4I27A card uses interrupts, the specific IRQ line driven by the 4I27A is determined by the ISEL bits of the 82C55's port A. The four ISEL bits form a binary code that corresponds directly with the IRQ line selected. Since there are other bits on the 82C55's port A, you should always change the ISEL bits by reading the port, modifying the desired bits, and then rewriting the port. The Pascal source code in the file 4I27LOW.PAS has examples of setting the IRQ line.

### INTERRUPT MODE

The two IMODE bits on the 82C55's port A determine the 4I27A's interrupt mode. The IMODE bits function as follows:

IMODE1	IMODE0	FUNCTION
0	0	Interrupt driver disabled
0	1	Normal interrupt mode
1	0	Shared interrupt mode
1	1	Shared interrupt mode + pull down

The shared interrupt mode uses an active pullup - passive pulldown driver to allow the sharing (logical or'ing) of interrupts between cards. The interrupt can only be shared with other cards that support this mode of shared interrupts.

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## OPERATION

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### INTERRUPT OPERATION

The LM628 interrupt outputs are logically ORed by the 4I27A card. This means that an interrupt output from either of the LM628's will can cause a system interrupt. 4I27's with revision C or greater PC cards can read the interrupt status of both LM628s though port B of the parallel interface. This can be used by the interrupt service routine to quickly determine which channel caused the interrupt.

The bus interrupt line is driven by a tri-state buffer controlled by the IMODE bits. The IMODE bits will be in a low state until the 82C55 is programmed. If interrupts are not used, the IMODE bits should be low.

### CONNECTING MOTORS

After the 4I27A has been configured and installed in the system, it should be checked for correct operation. The supplied programs 4I27READ, and 4I27TUNE can be used as a quick check of 4I27A functionality.

The first step is to connect the 4I27A to the quadrature encoder(s) and then use 4I27READ to check for functionality and proper count direction (to match the coordinates of your positioning system).

The program 4I27READ simply prints both motor positions (relative to their position at program initialization) continuously until a key is pressed. If the count direction is reversed, switch the encoder A and B outputs to the 4I27A. When RS-422 interface encoders are used, make sure not to mix A and B outputs.

For the next step, It is suggested that the motor drive leads be left disconnected, and 4I27TUNE be run. When 4I27TUNE is run, it will check for the presence of the 4I27A, and enable motor 0. Motor 0 will be configured for position mode with the home position being set to wherever motor 0 happens to be during program initialization.

The analog outputs of the 4I27A can then be monitored while the motor is manually 'rocked' back and forth from its original position. If the 4I27A is operating correctly, the analog outputs should vary with encoder shaft position.

The motor(s) should be connected such that the motor drive would tend to oppose the hand generated motor motion.

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## OPERATION

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### SERVO SYSTEM TUNING

A simple manual tuning program, 4I27TUNE, is supplied with the 4I27A. This program allows you to manually adjust the digital filter parameters while the selected motor is in operation. In addition, 4I27TUNE can record the step response of the servo system with the current set of filter parameters. 4I27TUNE uses the cursor keys to select the desired parameter, and adjust that parameter. The selectable parameters are as follows:

PARAMETER	DESC.	RANGE	UNITS
KP	gain	0 to 32767	drive/err
KD	damping	0 to 32767	drive/err(n)-error(n-1)
KI	integral	0 to 32767	drive/accum. error
IL	integral limit	0 to 32767	KI contribution limit
SI	sample interval	0 to 255	SP/(SI+1)
STEP SIZE	motor step	100 to 10000	encoder counts
VELOCITY	velocity	0 to 1073741823	encoder counts/SP
ACCELERATION	acceleration	0 to 1073741823	encoder counts/SP/SP
MOTOR	selects motor	0 and 1	
TIME/DIV	selects graph	10 to 200	milliseconds/division

SP = SAMPLING PERIOD = 256 uSec \* (1+SI) with 8 MHz clock.

The following table shows the 4I27TUNE key functions.

KEY	FUNCTION
UP ARROW	select parameter
DOWN ARROW	select parameter
RIGHT ARROW	adjust parameter up fast (+10%)
LEFT ARROW	adjust parameter down fast (-10%)
PAGE DOWN	adjust parameter up slow (+1%)
END	adjust parameter down slow (-1%)
PAGE UP	draw horizontal timing scale
INSERT	do step and record response
DELETE	clear graph
CTRL HOME	reset parameters to initial values (current motor only)
ALT X	exit program

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## OPERATION

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### DEMONSTRATION PROGRAMS

4I27VELO sets motor 0 and motor 1 into the velocity mode and allows you to set their speeds in RPM.

4I27POS1 sets motor 0 to position mode, and does some ramp-up / ramp- down motions. 4I27POS2 sets motor 0 and 1 to position mode, and does a set of coordinated (XY) motions with them. 4I27POS2 will bail out if both motors are not present.

4I27VELO and 4I27POS may need to have their default filter parameters and other constants changed to match your motors and card location before being run.

4I27TUNE is a manual parameter tuning program that allows you to optimize filter parameters while the selected motor is in operation. 4I27TUNE requires an EGA, VGA or Hercules compatible video adaptor card for operation.

All of the demonstration programs are written in Turbo Pascal. The source code for the demonstration programs is provided in the source directory of the 4I27A distribution disk.

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**REFERENCE INFORMATION**


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**SPECIFICATIONS**

	MIN	MAX	UNIT	NOTES
<b>POWER SUPPLY</b>				
Voltage	4.5	5.5	V	
Supply current	---	125	mA	(no ext load)
<b>BUS LOADING:</b>				
Input capacitance	---	15	pF	
Input leakage current	---	5	uA	
Output drive capability	150	---	pF	
Output sink current	12	---	mA	
<b>I/O PORT LOADING:</b>				
Input logic low	-.3	.8	V	
Input logic high	2.0	5.5	V	
Output low	----	.4	V	2.5 mA sink
Output high	3.0	---	V	2.5 mA source
<b>ANALOG OUT:</b>				
Output voltage	-10	+10	V	5K Ohm load
<b>ENVIRONMENTAL:</b>				
Operating temperature range				
-I version	-40	+85	°C	
-C version	0	+70	°C	
Relative humidity	0	90	%	Non-condensing

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## REFERENCE INFORMATION

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### WARRANTY

Mesa Electronics warrants the products it manufactures to be free effects in material and workmanship under normal use and service for the period of 2 years from date of purchase. This warranty shall not apply to products which have been subject to misuse, neglect, accident, or abnormal conditions of operation.

In the event of failure of a product covered by this warranty, Mesa Electronics, will repair any product returned to Mesa Electronics within 2 years of original purchase, provided the warrantor's examination discloses to its satisfaction that the product was defective. The warrantor may at its option, replace the product in lieu of repair.

With regard to any product returned within 2 years of purchase, said repairs or replacement will be made without charge. If the failure has been caused by misuse, neglect, accident, or abnormal conditions of operation, repairs will be billed at a nominal cost.

<p>THE FOREGOING WARRANTY IS IN LIEU OF ALL OTHER WARRANTIES, EXPRESS OR IMPLIED. INCLUDING BUT NOT LIMITED TO ANY IMPLIED WARRANTY OF MERCHANTABILITY, FITNESS, OR ADEQUACY FOR ANY PARTICULAR PURPOSE OR USE. MESA ELECTRONICS SHALL NOT BE LIABLE FOR ANY SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES, WHETHER IN CONTRACT, TORT, OR OTHERWISE.</p>
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**If any failure occurs, the following steps should be taken:**

1. Notify Mesa Electronics, giving full details of the difficulty. On receipt of this information, service data, or shipping instructions will be forwarded to you.
2. On receipt of the shipping instructions, forward the product, in its original protective packaging, transportation prepaid to Mesa Electronics. Repairs will be made at Mesa Electronics and the product returned transportation prepaid.

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**REFERENCE INFORMATION**

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**SCHEMATICS**









